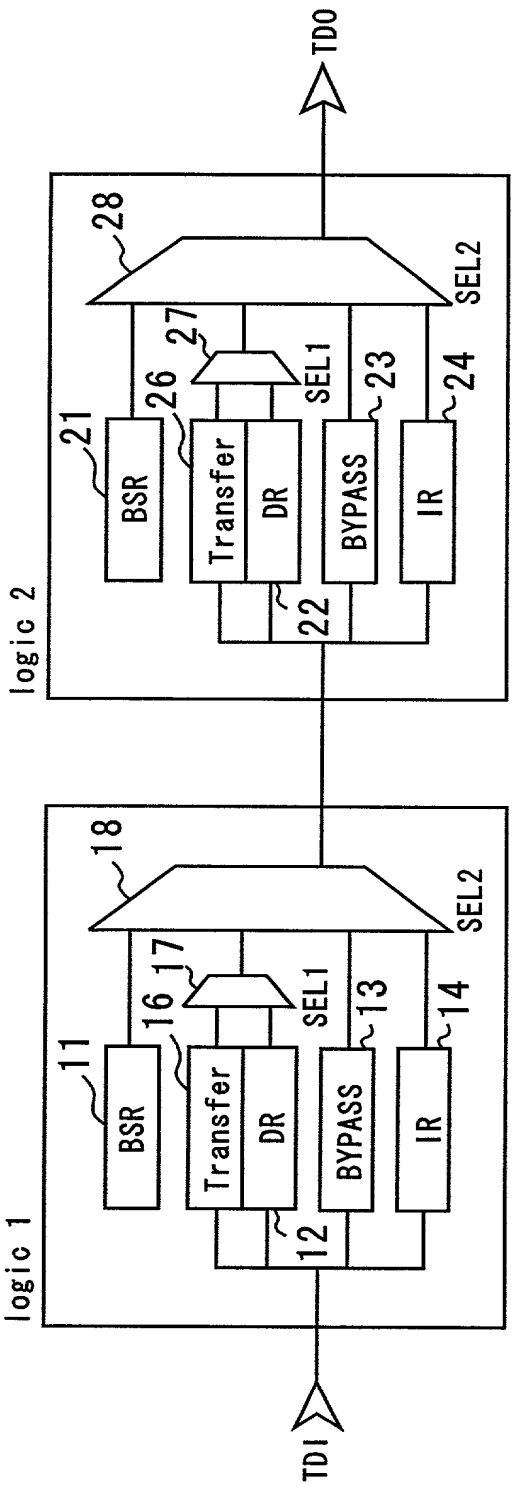

TDO
TDI
logic 2
logic 1
28
SEL2
27
SEL1
21
BSR
26
Transfer
DR
22
BYPASS
23
IR
24
18
SEL2
17
SEL1
11
BSR
16
Transfer
DR
12
BYPASS
13
IR
14

Fig. 1

Fig. 2

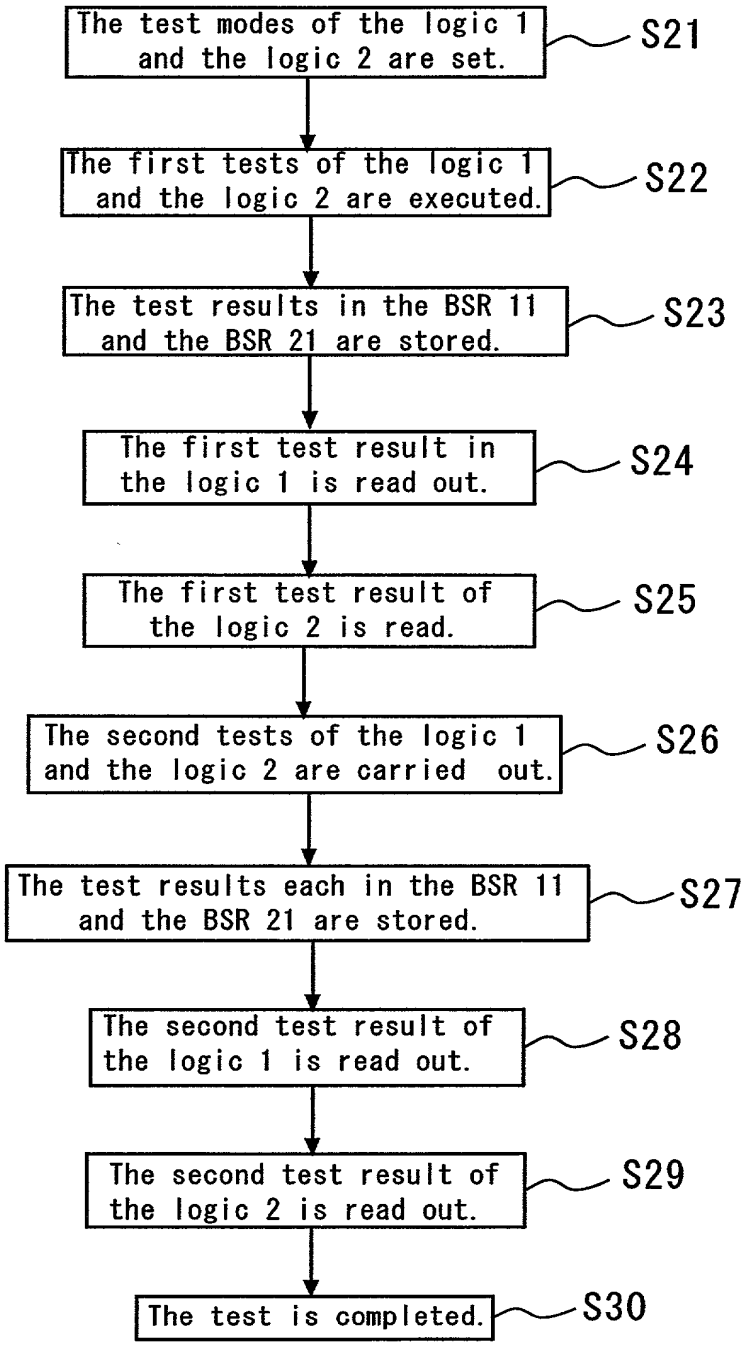
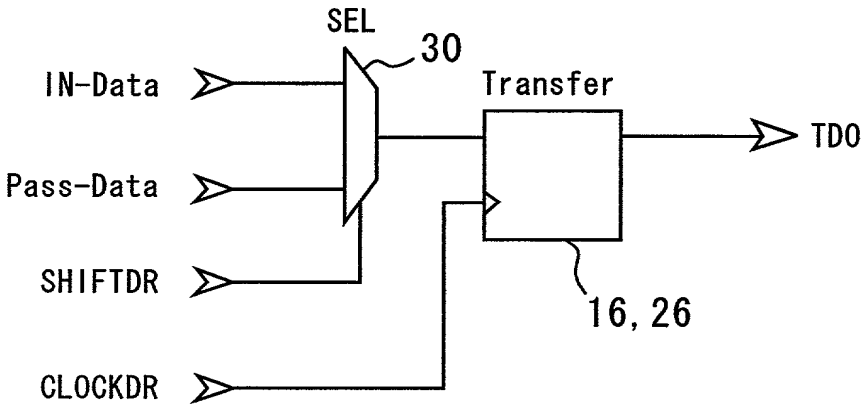

The test modes of the logic 1 and the logic 2 are set.
S21
The first tests of the logic 1 and the logic 2 are executed.
S22
The test results in the BSR 11 and the BSR 21 are stored.
S23
The first test result in the logic 1 is read out.
S24
The first test result of the logic 2 is read.
S25
The second tests of the logic 1 and the logic 2 are carried out.
S26
The test results each in the BSR 11 and the BSR 21 are stored.
S27
The second test result of the logic 1 is read out.
S28
The second test result of the logic 2 is read out.
S29
The test is completed.
S30

Fig. 3



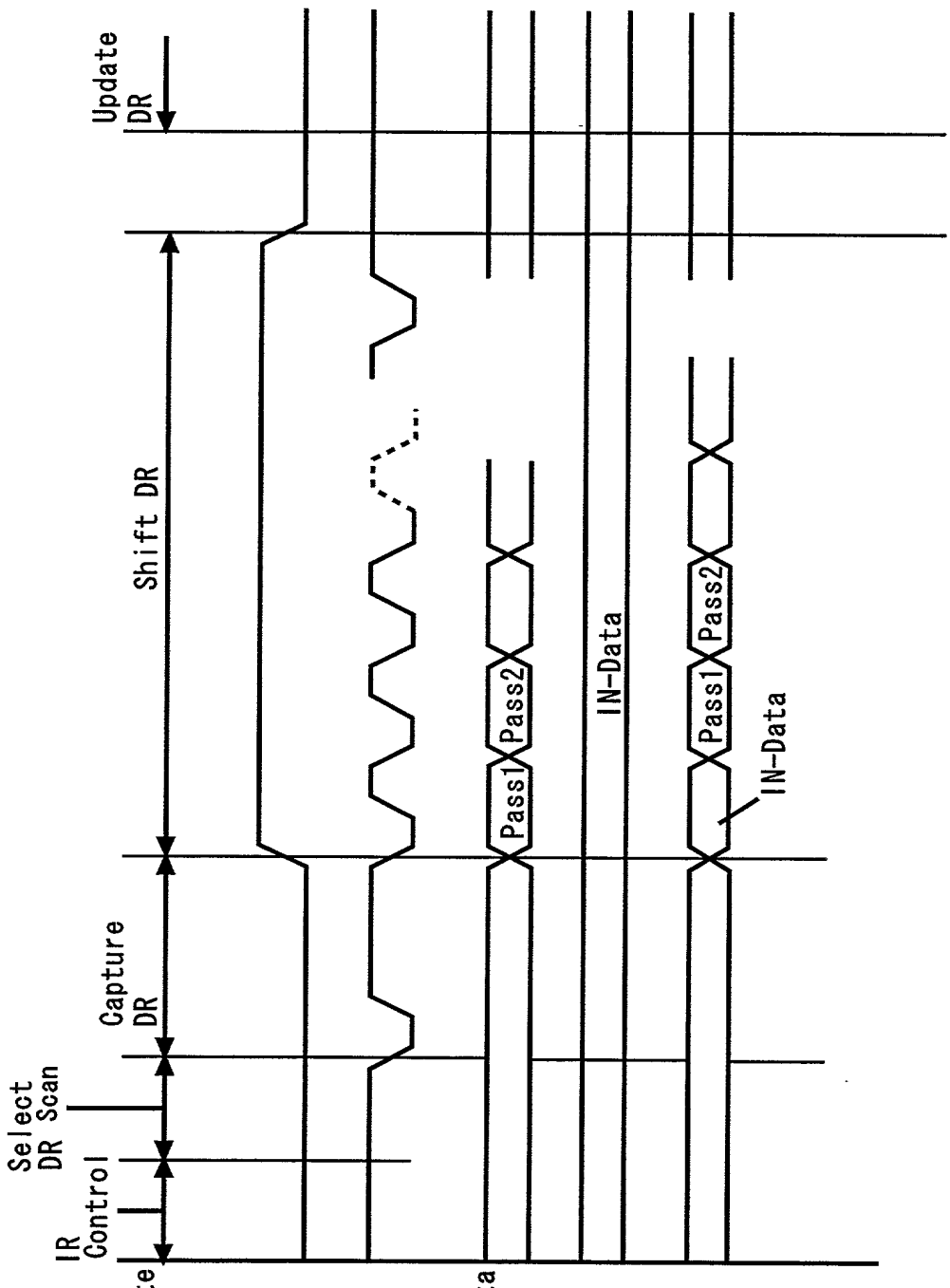


Fig. 4A TAP-State

Fig. 4B SHIFTDR

Fig. 4C CLOCKDR

Fig. 4D Pass-Data

Fig. 4E IN-Data

Fig. 4F TDO

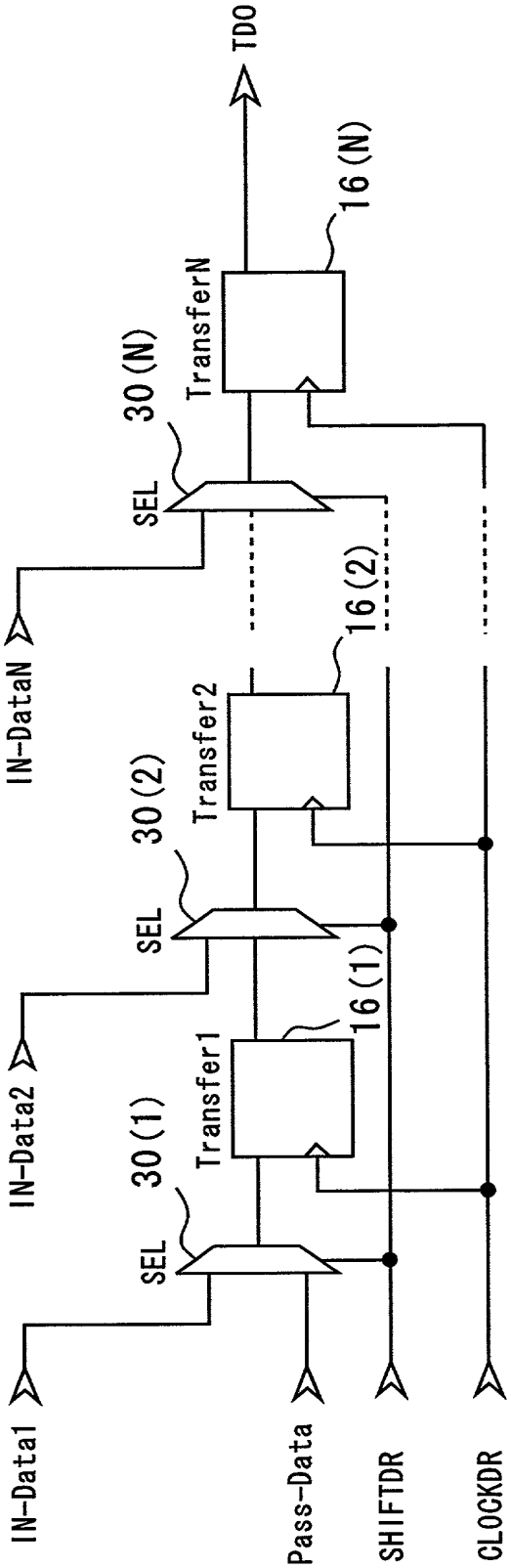

TDO
16 (N)
TransferN
30 (N)
SEL
IN-DataN
16 (2)
Transfer2
30 (2)
SEL
IN-Data2
16 (1)
Transfer1
30 (1)
SEL
IN-Data1
Pass-Data
SHIFTDR
CLOCKDR

Fig. 5

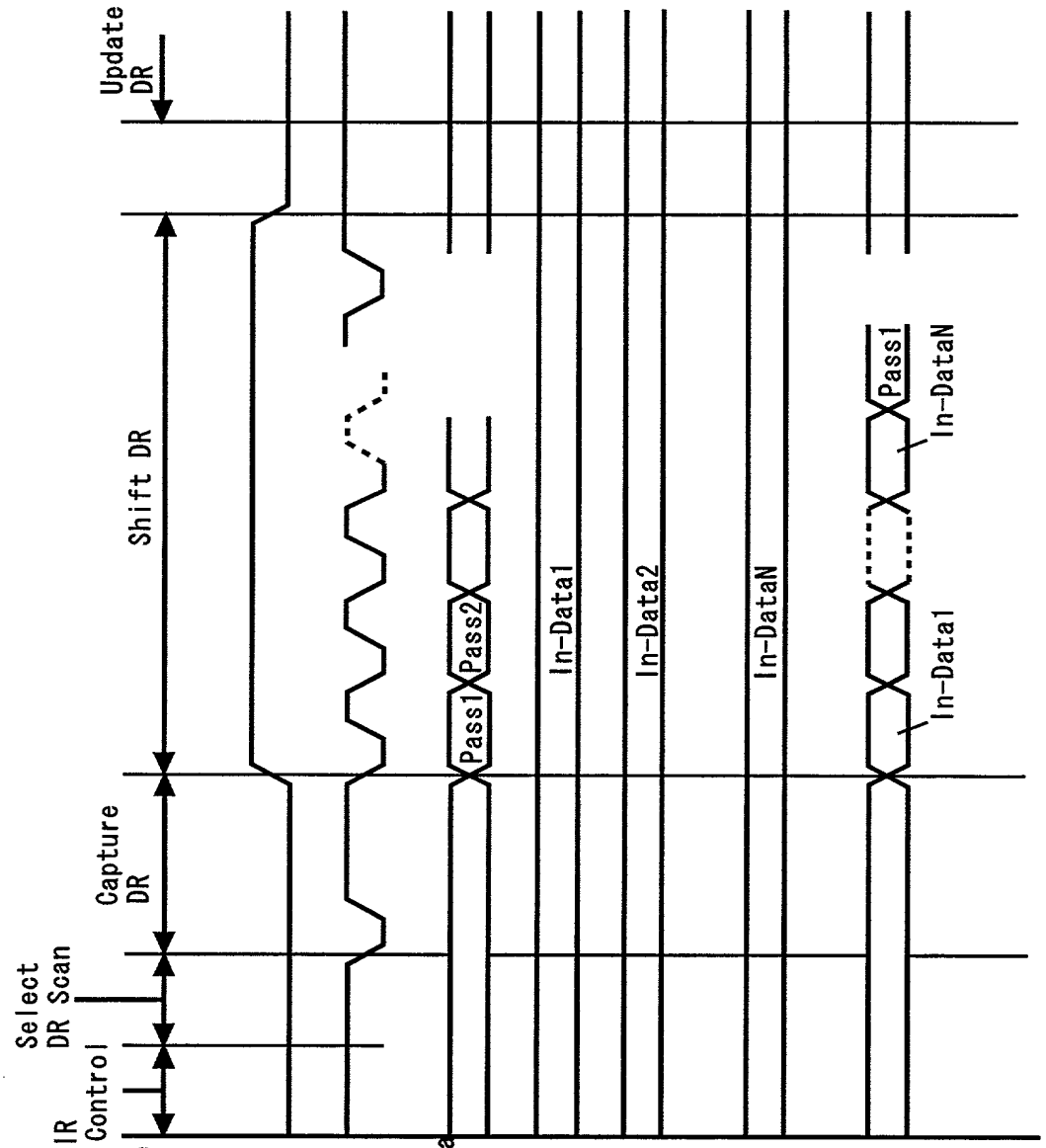


Fig. 6A TAP-State

Fig. 6B SHIFTDR

Fig. 6C CLOCKDR

Fig. 6D Pass-Data

Fig. 6E In-Data1

Fig. 6F In-Data2

Fig. 6G In-DataN

Fig. 6H TDO

Fig. 7

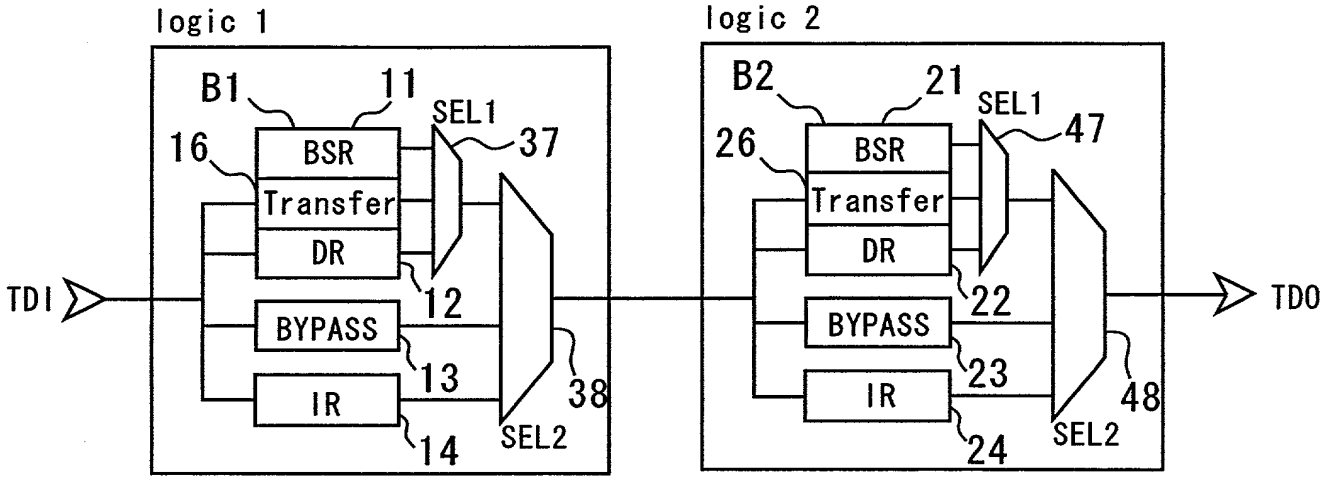

logic 1
logic 2
B1
11
SEL1
16
BSR
37
Transfer
DR
12
TDI
BYPASS
13
38
IR
14
SEL2
B2
21
SEL1
26
BSR
47
Transfer
DR
22
TDO
BYPASS
23
IR
48
24
SEL2

Fig. 8

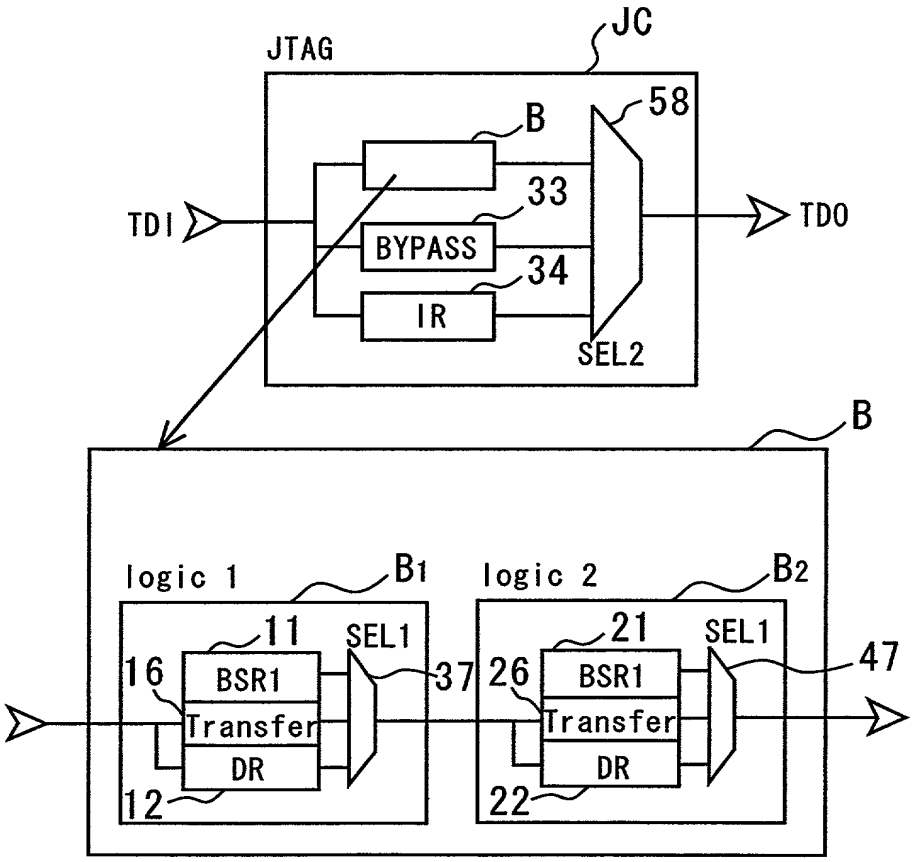

JTAG
JC
B
58
TDI
33
TDO
BYPASS
34
IR
SEL2
B
logic 1
B1
logic 2
B2
11
SEL1
21
SEL1
16
BSR1
37
26
BSR1
47
Transfer
Transfer
DR
DR
12
22

Fig. 9

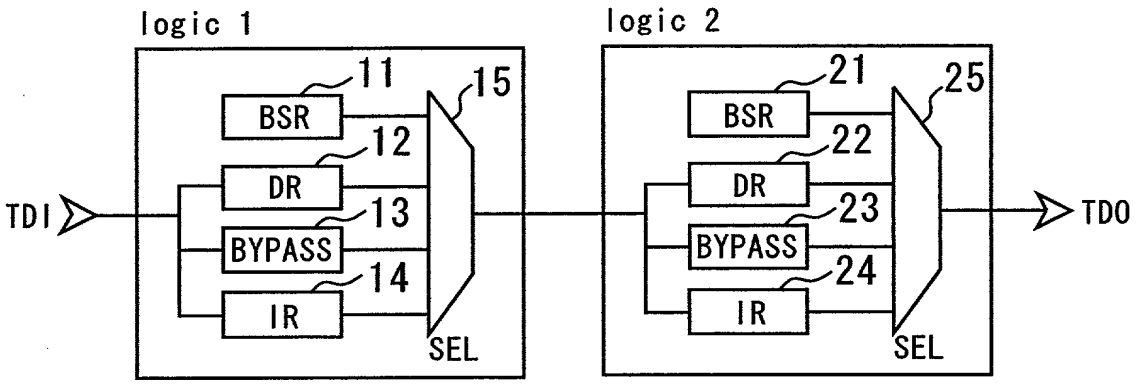
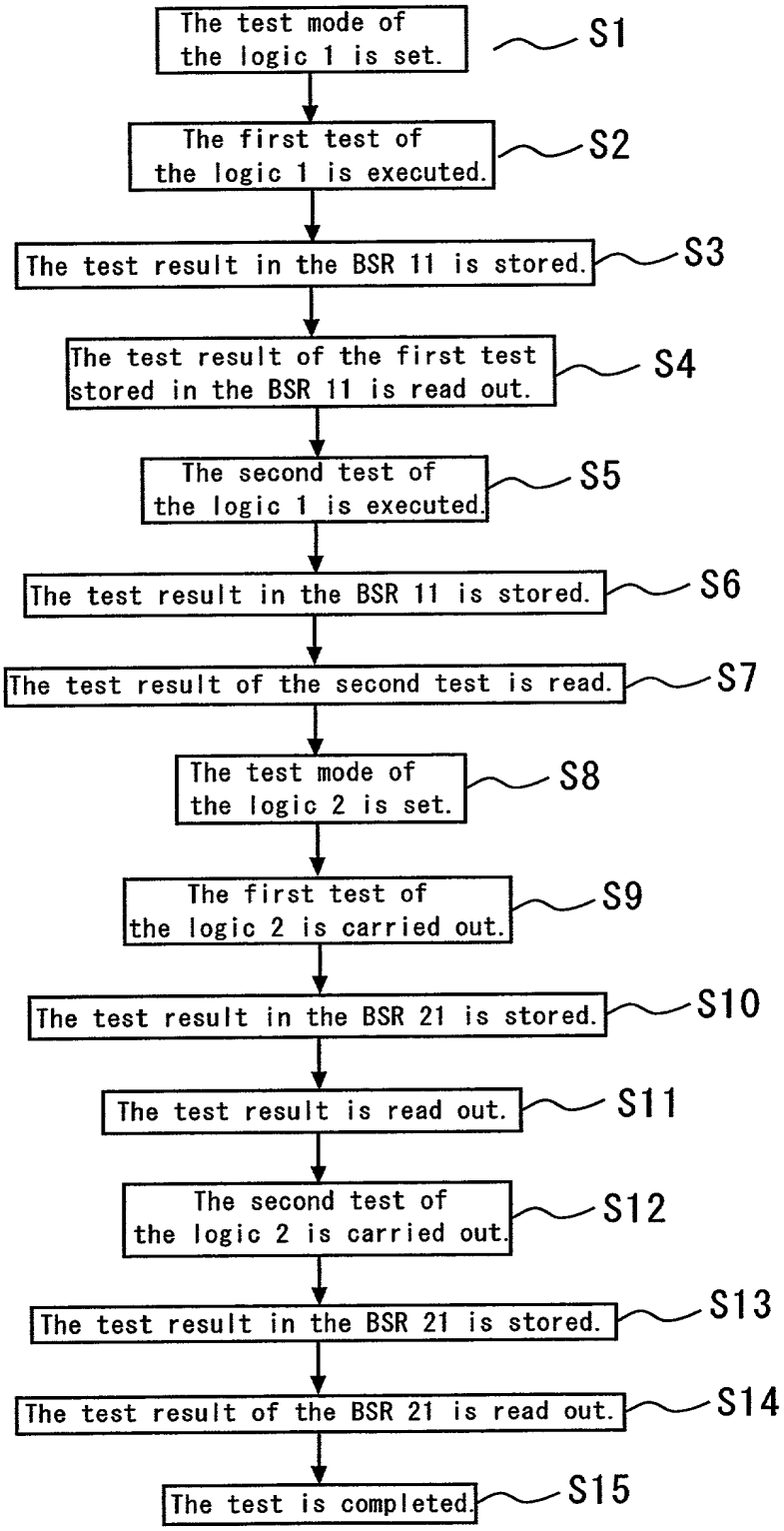

logic 1
logic 2
11
12
13
14
15
21
22
23
24
25
BSR
DR
BYPASS
IR
SEL
TDI
TDO

Fig. 10


The test mode of the logic 1 is set.
S1
The first test of the logic 1 is executed.
S2
The test result in the BSR 11 is stored.
S3
The test result of the first test stored in the BSR 11 is read out.
S4
The second test of the logic 1 is executed.
S5
The test result in the BSR 11 is stored.
S6
The test result of the second test is read.
S7
The test mode of the logic 2 is set.
S8
The first test of the logic 2 is carried out.
S9
The test result in the BSR 21 is stored.
S10
The test result is read out.
S11
The second test of the logic 2 is carried out.
S12
The test result in the BSR 21 is stored.
S13
The test result of the BSR 21 is read out.
S14
The test is completed.
S15